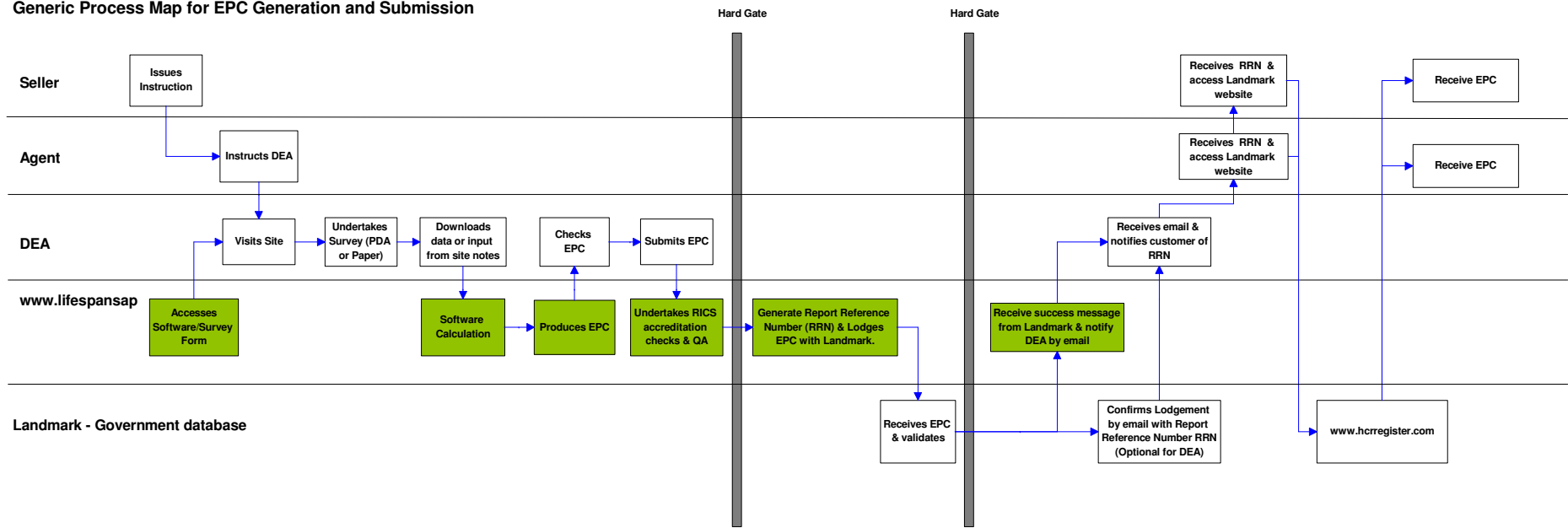


Generic Process Map for EPC Generation and Submission



Note: Hard Gate Failure Processes Not Included in above